

- (a) **Name** DR. SATVIR SINGH SIDHU
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- (c) **Specialization** Electronics & Communication Engineering
- (d) **Date of Birth** 07-12-1975
- (e) **Memberships** Member IETE, Life Member ISTE
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- (i) **Academic Qualification**



(a) Doctor of Philosophy	Topic: Investigation in Evolution of Type-1 and Type-2 Fuzzy Systems Department: Electronics & Communication Engineering University: Maharshi Dayanand University, Rohtak Year: 2005-2011
(b) Master of Engineering	Percentage: 75.33% Department: Electronics & Communication Engineering College: Delhi College of Engineering, Delhi University: University of Delhi, Delhi Batch: 1998-2000
(c) Bachelor of Technology	Percentage: 63.84% Department: Electronics & Communication Engineering College: Dr B R Ambedkar National Institute of Technology, Jalandhar University: Punjab Technical University, Jalandhar Batch: 1994-1998

- (j) **Total Teaching Experience** 10+ Years [Approx.]

Designation	From	To	Institute	Year/Month
(a) Part-Time Lecturer	01.10.1999	21.12.1999	GND Co-Ed. Polytechnic, Sec 15, Rohini, Delhi-85	00/03
	01.02.2000	30.06.2000	Delhi College of Engg, Shahbad Dairy, Delhi	00/05
(b) Lecturer/Sr. Lecturer	03.08.2000	30.09.2004	BRCM College of Engg & Tech, Bahal, Bhiwani, Haryana	04/02
(c) Assistant Professor	01.10.2004	22.07.2006	BRCM College of Engg & Tech, Bahal, Bhiwani, Haryana	01/10
	24.07.2006	01.07.2009	SBS College of Engg & Tech, Moga Road, Ferozepur, Punjab	03/00
(d) Associate Professor	24.07.2009	Till Date	SBS College of Engg & Tech, Moga Road, Ferozepur, Punjab	

- (k) **Short-Term Training Programmes Attended (Total 9 Weeks)**

(a) Managerial and Communicative Skills (CRSCE Murthal)	09.07.2001	20.07.2001	2 Weeks
(b) VHDL and System Design (TTTI Chandigarh)	08.12.2003	12.12.2003	1 Week
(c) Accreditation of Technical Institutions (NITTR Chandigarh)	13.12.2004	17.12.2004	1 Week
(d) Fuzzy Control: From Analytical Foundations to Applications in Chaos in Fuzzy Logic Controllers (CRSCE Murthal)	02.08.2004	14.08.2004	2 Weeks
(e) Intelligent Systems & Mobile Adhoc Networks (DCRUST Murthal)	10.12.207	14.12.2007	1 Week
(f) Embedded Systems and VLSI Design in Robotics (DAVIET Jalandhar)	12.07.2010	23.07.2010	2 Weeks
(g) Workshop on Analog and Digital ASIC Design (Kurukshetra University, Kurukshetra)	04.05.2002	05.05.2002	2 Days
(h) ANN, FL and Other Computational Techniques (NITTTT Chandigarh)	16.08.2010	20.08.2010	1 Week
(i) Intelligent Computational Techniques (SBS College of Engg. & Tech., Ferozpur)	04-07-2011	15-07-2011	2 Weeks

(l) **Tutorials/Expert Talks Presented in Conferences/AICTE STTP**

(a) Type-2 Fuzzy Systems...Modeling and Simulations (ISTK Klawad, Yamunanagar, HR)	3 rd International Conference on Intelligent Systems and Networks (14-16.02.2009)
(b) Evolutionary Design of Fuzzy Models (T1 and T2) (IITM Gwalior, MP)	AICTE Sponsored STTP on Multi-objective Optimization using Evolutionary Algorithms
(c) T2 Fuzzy Logic Systems (ISTK Klawad, Yamunanagar, HR)	4 th International Conference on Intelligent Systems and Networks (26-28.02.2010)
(d) Particle Swarm Optimization (Bharat Institute of Technology, Sonapat)	National Workshop on Intelligent Systems and Wireless Networks (20-24.12.2011)
(e) T2 Fuzzy Systems...Modeling & Simulations (ISTK Klawad, Yamunanagar, HR)	5 th International Conference on Intelligent Systems and Networks (18-20.02.2011)

(m) **Paper Presented in International/National Conferences**

(a) Mobile Robot Navigation using IT2 FLS	IEEE National Conference on Applications of Intelligent Systems (13-15.03.2008)
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(n) **Short-Term Training Programmes Conducted**

(a) Faculty Develop Programme on System Designing using OrCAD PSPICE (BRCM College of Engg & Tech, Behal, HR)	15.05.2006	20.06.2006	1 Week
(b) AICTE Sponsored SDP on Intelligent Computational Techniques (SBS College of Engineering & Technology, Moga Road, Ferozpur)	04.07.2011	15.07.2011	2 Weeks

(o) **Sport/Extra-Curricular/Co-curricular Activities**

(a) Zonal Middle School Tournament (Phillaur-I)	Hockey	1989-90	Winner
(b) Tehsil Science Fair (Distt. Kapurthala)	Quiz-Test	1991-92	First
(c) District Science Fair (Distt. Kapurthala)	Multi-purpose Madhani	1993-94	-
(d) District Science Fair (Distt. Kapurthala)	Quiz-Test	1993-94	-
(e) 5 th Annual Athletic Meet	1500m	1994-95	Third
(f) 5 th Annual Athletic Meet	5000m	1994-95	Third

(g) 6 th Annual Athletic Meet	1500m	1995-96	Second
(h) 5 th Annual Athletic Meet	5000m	1995-96	Third
(i) Inter-year Hockey Tournament	Hockey	1997-98	First

(p) **Subjects Taught at:**

(a) PG Level

(b) UG Level

- Neural Networks and Fuzzy Logic
- Signal and Systems
- Digital Signal Processing
- Digital Electronics
- Analog Electronics
- Network Analysis & Synthesis
- Communication System
- Electronic Measurements & Instrumentation
- Intelligent Instrumentation
- VLSI Design Technology

(q) **No. of papers published at:**

i) Masters' Level

- **International Conferences** One
Satvir Singh and Dr. Arun Khosla, "Fuzzy Logic Based Stadium Lights Controller," in *Proc. December 1999 International Conference on Cognitive Systems*, pp 618-624.

ii) Ph. D. Level

- **International Conferences** Three
Satvir Singh & Dr. J. S. Saini, "Captive Power Management using Op-Amp Based FLC," in *Proc. February 2006 Asian Conference on Intelligent Systems & Networks*, pp 160-166
Satvir Singh & Dr. J. S. Saini, "Fuzzy FPGA Based Captive Power Management", in *Proc. April 2006 IEEE Power India Conference*, Available online www.ieeexplore.org
- **Satvir Singh**, Dr. J. S. Saini, Vikram Mutneja & Neeraj Gill, "Mobile Robot Navigation using IT-2 FLS", in *Proc. 2008 IEEE National Conference on Applications of Intelligent Systems (AIS-2008)*, HCE Sonipat, India, pp.

8. **Projects Carried out**

- Captive Power Management
- Auto-Changer for Mains & D.G. Supply
- 4-Line Telephone Exchange
- Analog Fuzzy Logic Processor
- Remote operated Switch Board

Dr. Satvir Singh Sidhu